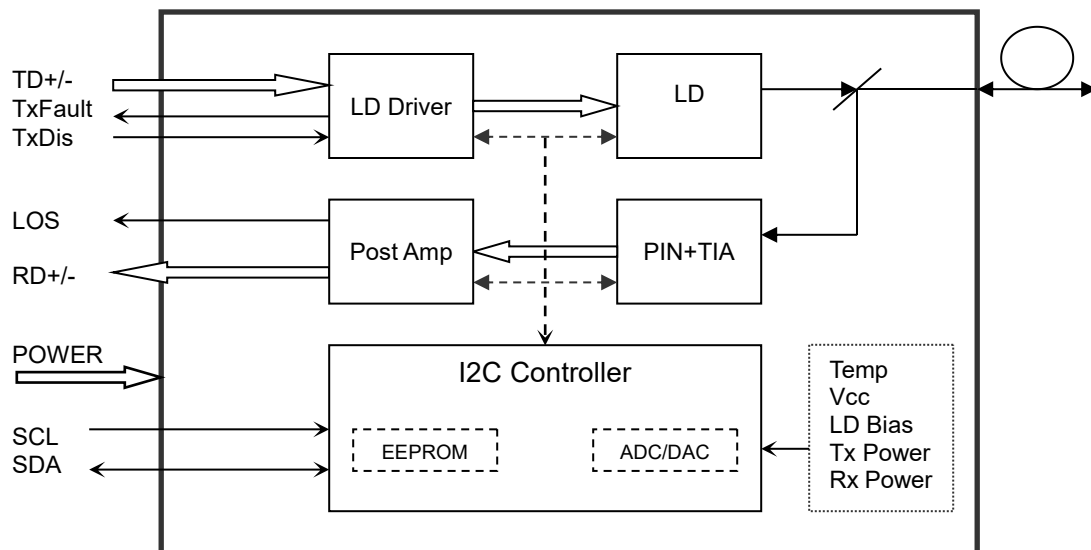




Transceiver functional diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	0		+70	°C
	Extended	-20		+85	°C
	Industrial	-40		+85	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			300	mA
Data Rate			2.5		Gbps

Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λ_c	1470	1490	1510	nm	
Spectral Width (-20dB)		$\Delta\lambda$			1	nm	
Side-Mode Suppression Ratio		SMSR	30			dB	
Average Output Power		Pout	-5		0	dBm	1
Extinction Ratio		ER	9.0			dB	
Data Input Swing Differential		VIN	180		1200	mV	2
Input Differential Impedance		ZIN	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	
	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λ_c	1260	1310	1360	nm	
Receiver Sensitivity					-18	dBm	3
Receiver Overload			-1			dBm	3
LOS De-Assert		LOSD			-20	dBm	
LOS Assert		LOSA	-38			dBm	
LOS Hysteresis			0.5		4	dB	
Data Output Swing Differential		Vout	600	800	1000	mV	4
LOS	High		2.0		Vcc	V	
	Low				0.8	V	

Notes:

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2²³-1 test pattern @2488Mbps, BER ≤1×10⁻¹².
4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t _{on}			1	ms
Tx Disable Assert Time	t _{off}			10	μs
Time To Initialize, including Reset of Tx Fault	t _{init}			300	ms
Tx Fault Assert Time	t _{fault}			100	μs
Tx Disable To Reset	t _{reset}	10			μs
LOS Assert Time	t _{loss_on}			100	μs
LOS De-assert Time	t _{loss_off}			100	μs
Serial ID Clock Rate	f _{serial_clock}		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		V _{cc}	V
MOD_DEF (0:2)-Low	V _L			0.8	V

Diagnostics

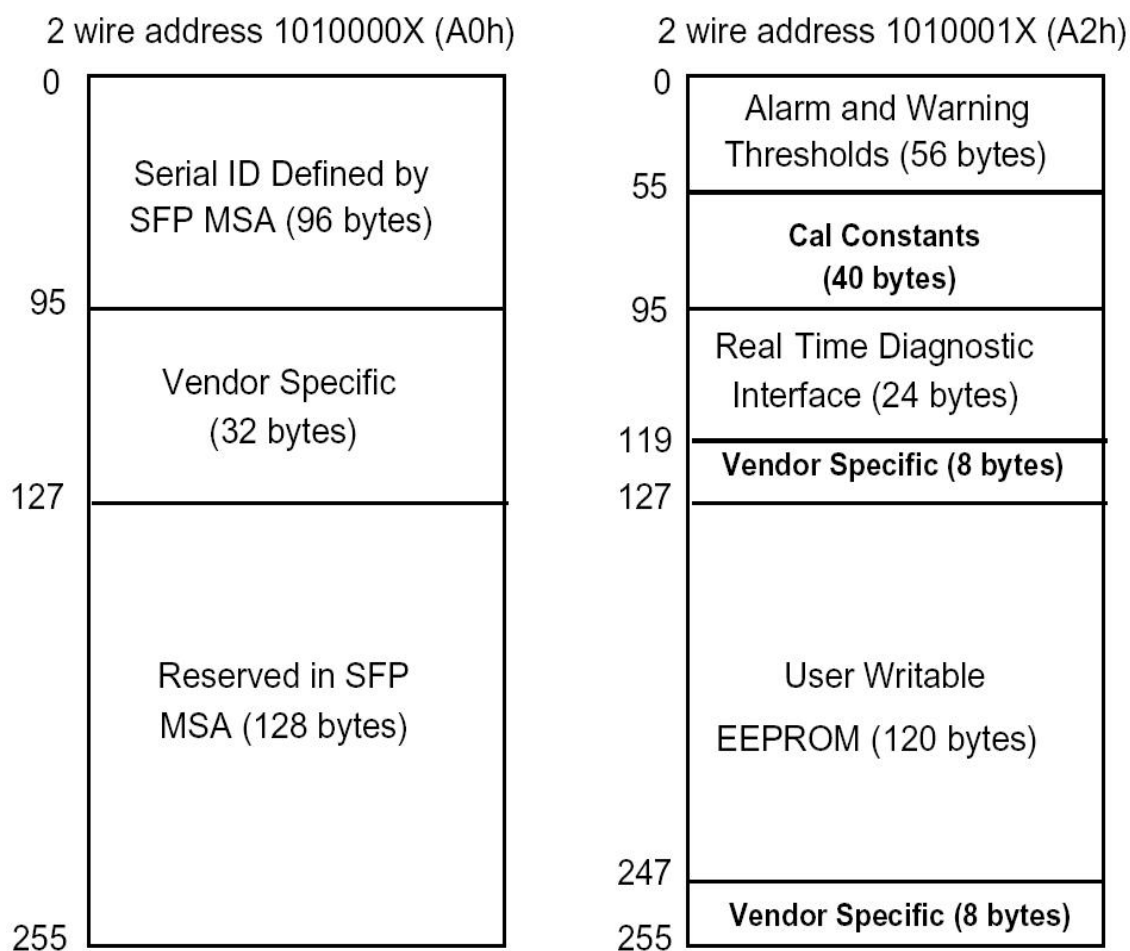
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
	-20 to +85			
	-40 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-5 to 0	dBm	±3dB	Internal
RX Power	-23 to -1	dBm	±3dB	Internal

Digital Diagnostic Memory Map

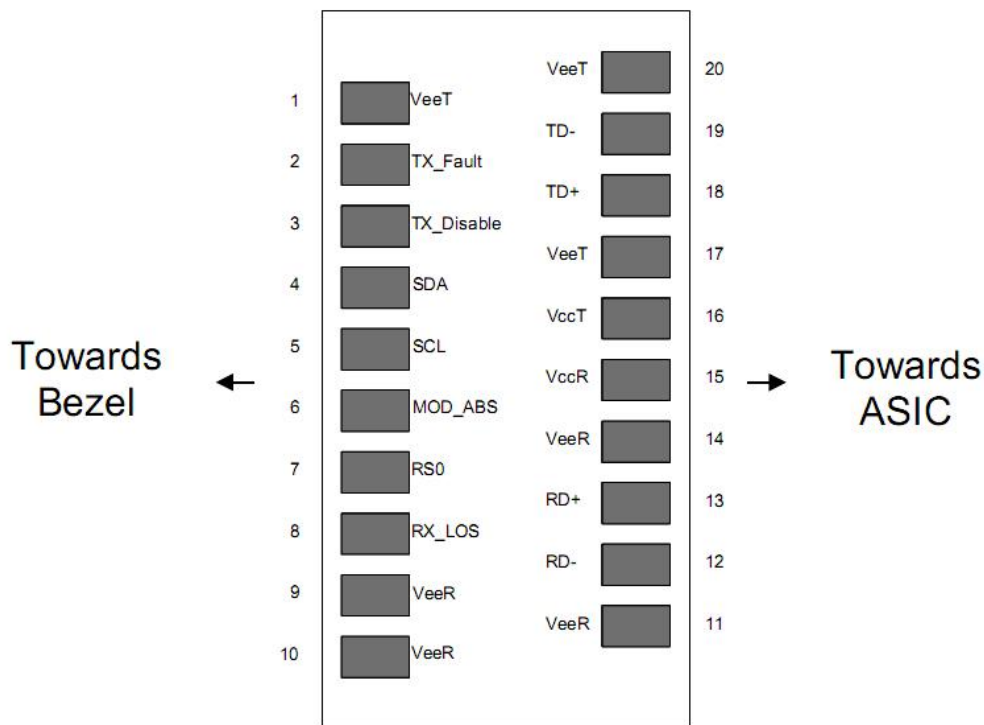
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Descriptions



Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	VEER	Receiver ground	1	
10	VEER	Receiver ground	1	
11	VEER	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	VEER	Receiver ground	1	
15	VCCR	Receiver Power Supply	2	
16	VCCT	Transmitter Power Supply	2	

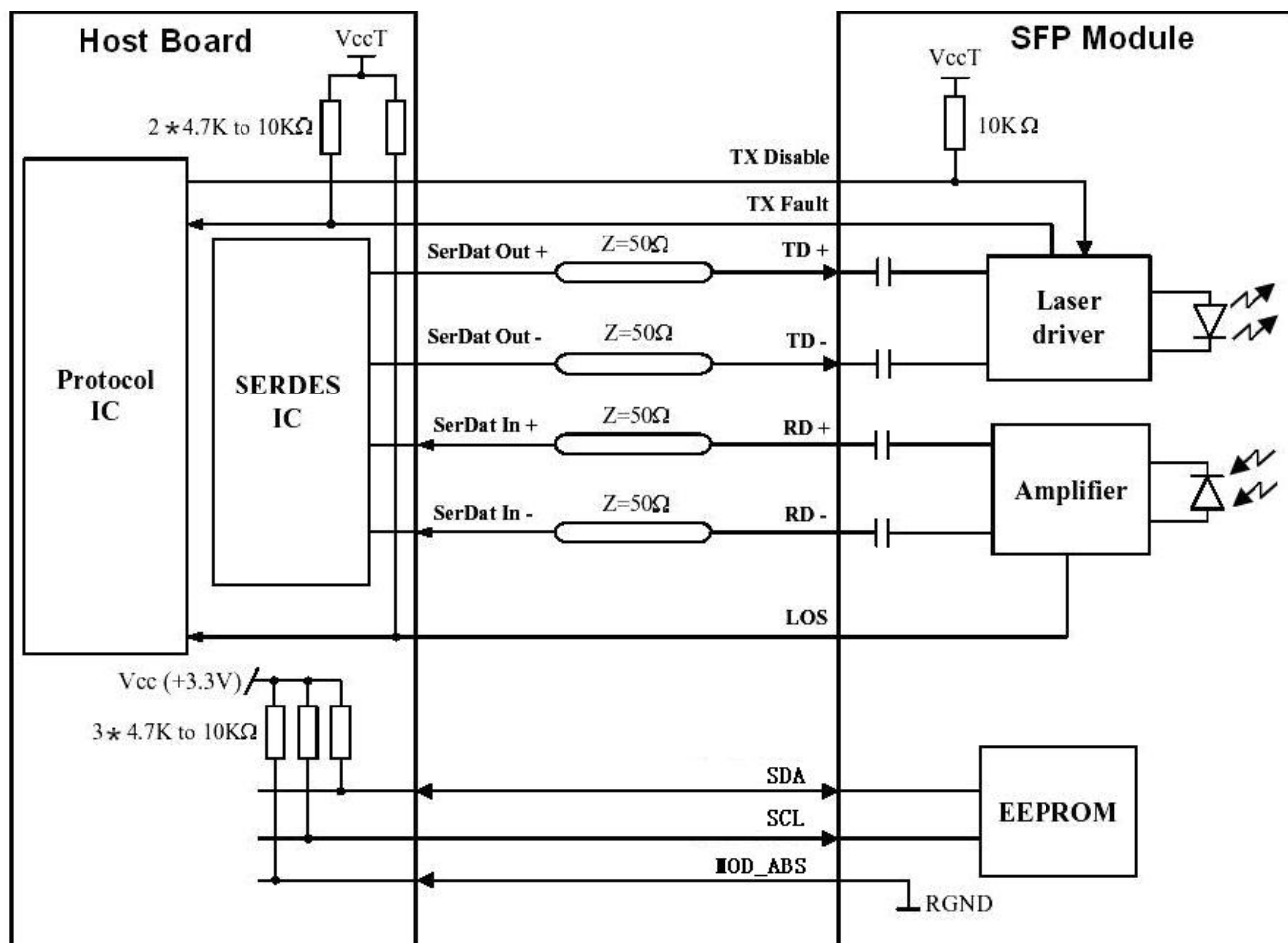
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	VEET	Transmitter Ground	1	

Notes:

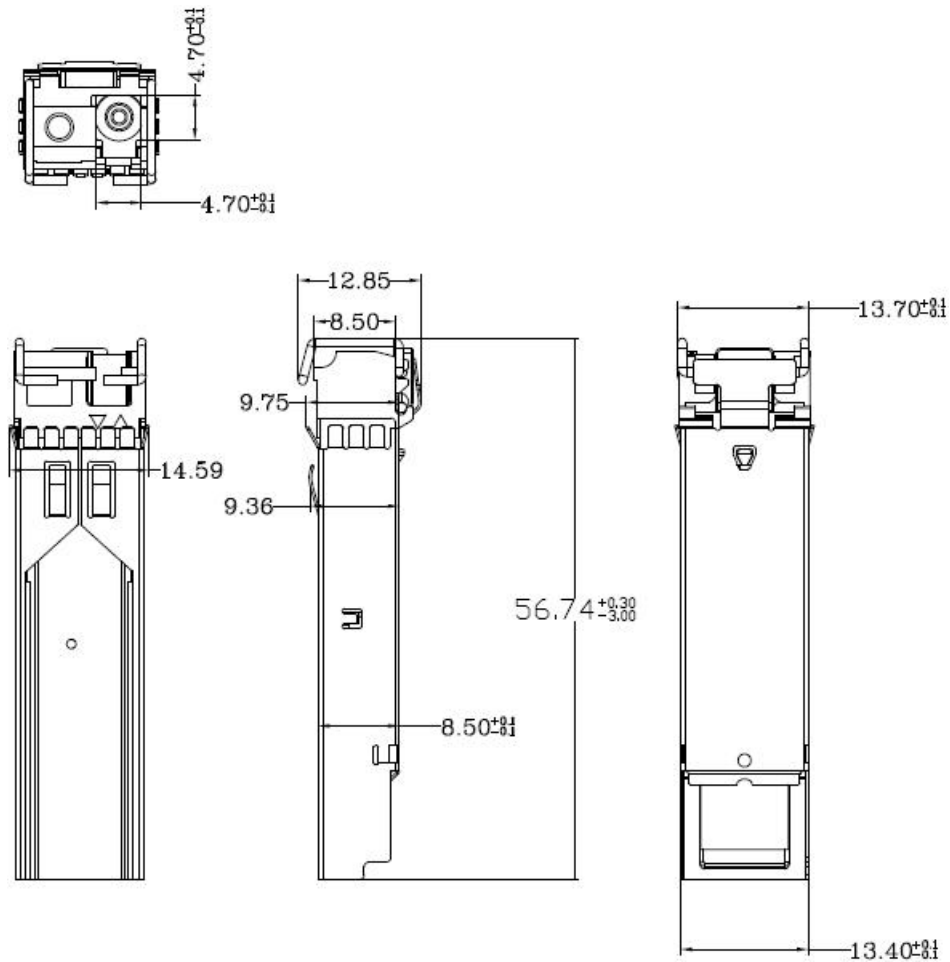
Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
LS-BL49312G-10C	1490T/1310R, 2.5Gbps, LC, 10km, 0°C~+70°C, with DDM
LS-BL49312G-10I	1490T/1310R, 2.5Gbps, LC, 10km, -40°C~+85°C, with DDM