

L9-TD3101-02CTC

1×9 155Mbps Transceiver

Features

- VCSEL/FP/DFB Laser Diode Transmitter
- Up to 2 Km on 50/125μm MMF
- 3.3V or 5V power supply
- SC/ST/FC duplex optical interface
- CML Differential Input /Output and (LV)TTL signal detect
- Class 1 Laser International Safety Standard IEC 825 Compliant
- Operating case temperature range (Standard: 0 to +70°C) or (Industrial: -40°C to +85°C)

Applications

- ATM 155Mbps
- SONET/SDH
- 100Mbps Ethernet
- Digital Video system
- Other optic link

Product Description

Link-pp's L9-TDxx01-xxCTC series of transceiver modules are the perfect solution for low-speed communication networks. These transceiver modules support data rates up to 200Mbps. The module is fully compliant with the Multi-sourced 1×9 protocol. These transceiver modules provides the system designer with products to implement a range of FDDI and ATM designs at the 100Mbps/125Mbps rate and SONET/OC-3, SDH/STM-1 for Telecommunication.

Absolute Maximum Ratings

Operating of the transceiver beyond the Absolute Conditions Listed in Table 1 will degrade or damage the product. It's not implied that the product would function above the recommended operating environment, it's possible to reduce the reliability and lifetime of device if Recommended Operating Environment is exceeded (Refer to table 2)

Table 1--- Absolute Maximum Conditions

Parameter		Symbol	Min	Max	Units	Note
Storage Temperature		T _{ST}	-40	+85	°C	-
Relative Humidity		RH	5	95	%	-
Supply Voltage	3.3V power	V _{CC}	0	+3.6	V	-
	5V power		0	+6		-

Table 2--- Recommended Operating Environment and Electrical Characteristics

Parameter		Symbol	Min	Typ	Max	Units	Note
Supply Voltage	3.3V power	V _{CC}	+3.1	+3.3	+3.5	V	-
	5V power		+4.75	+5.0	+5.25		-
Supply Current		I _{CC}	-	-	260	mA	-
Operating Case Temperature		T _{OP}	0	-	+70	°C	1
			-40	-	+85		2
Data Rate		B	10	155	200	Mbps	-
Soldering temperature		-	-	-	350	°C	3
Soldering duration		-	-	-	10	Sec	3

Note 1.Standard level
2.Industrial level
3.Not recommended wave soldering.

Optical Parameters

Table 3--- L9-TD3101-02CTC Transceiver Optical Characteristics
(Ambient Operating Temperature Ta=+25±5°C, VCC = 3.3 or 5.0±0.2V)

Parameter	Symbol	Min	Typ	Max	Units	Note
Transmitter						
Data rate	B	10	155	200	Mbps	-
Output Center Wavelength	λ_c	1260	1310	1360	nm	-
Output Spectral width (RMS)	λ	-	-	4	nm	-
Average Optical Output Power	P_o	-20	-	-14	dBm	1,2,3
		-18	-	-10		1,2,4
Extinction Ratio	ER	10	-	-	dB	1,2
Optic Output Eye: Compliant with Bellcore TR-NWT-000253 and ITU recommendation G.957						1,2
Receiver						
Receiver Sensitivity	P_{min}	-	-	-32	dBm	1,2
Receiver Saturation	P_{max}	-3	-	-	dBm	1,2
Operation Center Wavelength	λ_c	1260	-	1620	nm	-
SD	Deassert	PD	-45	-	dBm	1,2
	Assert	PA	-	-32	dBm	1,2
SD Hysteresis	SDHYS	0.5	-	-	dB	-

Note1: The optical power is launched into 62.5/125 μ m MMF.

Note2: With a PRBS 2²³-1 test pattern@ 155Mbps.

Note3: Average Optical Output Power range 1.

Note4: Average Optical Output Power range 2.

Electrical Parameters

Table 4--- Transceiver Electrical Characteristics

(Ambient Operating Temperature Ta=+25 $\pm$ 5 $^{\circ}$ C, VCC = 3.3 or 5.0 $\pm$ 0.2V)

Parameter	Symbol	Min	Typ	Max	Units	Note
Supply Voltage	3.3V power	V_{CC}	+3.1	+3.3	V	-
	5V power		+4.75	+5.0		-
Supply Current	I_{CC}	-	-	260	mA	-
Transmitter						
Differential Input Voltage	V_{in}	400	-	2000	mV _{P-P}	-
Input Differential Impedance	Z_{in}	90	100	110	Ω	-
Data Input Voltage - Low	V_{IL}	$V_{CC} - 1.81$	-	$V_{CC} - 1.48$	V	-
Data Input Voltage - High	V_{IH}	$V_{CC} - 1.17$	-	$V_{CC} - 0.88$	V	-

Receiver						
Differential Output Voltage	V_{OUT}	400	-	2000	mV _{P-P}	-
Data Output Voltage – Low	V_{OL}	$V_{CC} - 1.83$	-	$V_{CC} - 1.56$	V	1
Data Output Voltage – High	V_{OH}	$V_{CC} - 1.09$	-	$V_{CC} - 0.88$	V	1
SD Output Voltage-Low	V_{OL}	$V_{CC} - 2.0$	-	$V_{CC} - 1.56$	V	2
SD Output Voltage-High	V_{OH}	$V_{CC} - 1.1$	-	$V_{CC} - 0.75$	V	2
SD Output Voltage-Low	V_{OL}	0	-	0.8	V	3
SD Output Voltage-High	V_{OH}	2	-	V_{CC}	V	3

Note1: Outputs are terminated with 50Ω to VCC-2V&Related to VCC

Note2: (LV)PECL signal detect

Note3: (LV)TTL signal detect

Pin Definitions

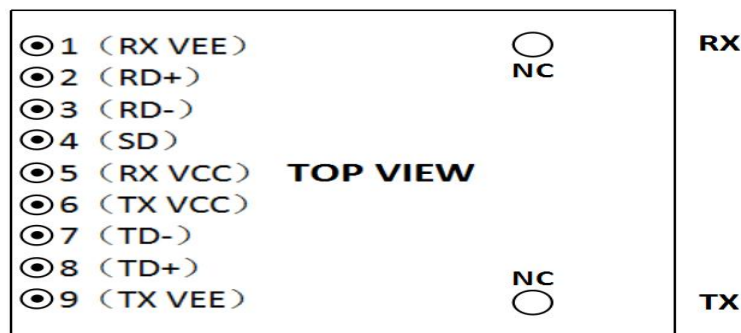
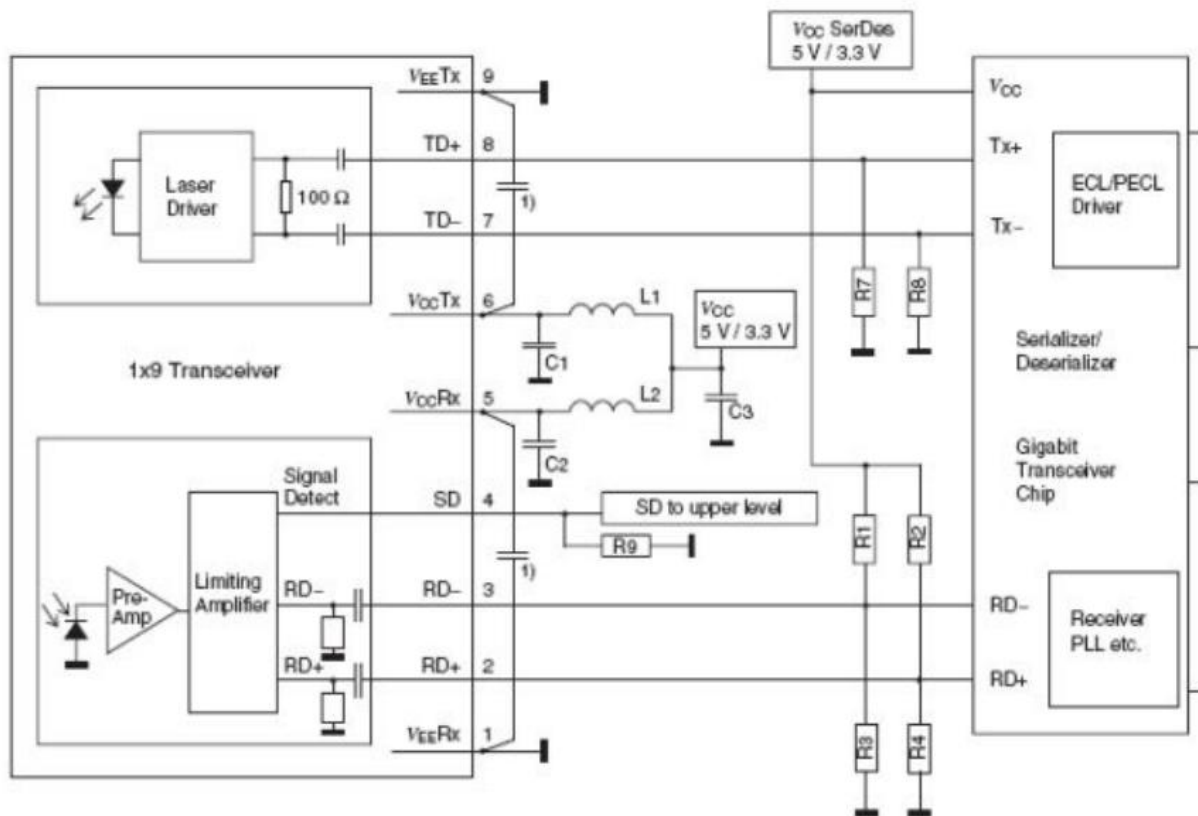


Table 5---Connector pin assignment

Pin #	Pin Name	Description	Note
1	RX VEE	Receiver Signal Ground	-
2	RD+	Receiver Data Out	-
3	RD-	Receiver Data Out Bar	-
4	SD	Signal Detect, (LV)TTL ouputs	1
5	RX VCC	Receiver Power Supply	-
6	TX VCC	Transmitter Power Supply	-
7	TD-	Transmitter Data In Bar	-
8	TD+	Transmitter Data In	-
9	TX VEE	Transmitter Ground	-

Note1: (LV)PECL-Normal optical input levels to the receiver result in logic “1” output, pull-down 130Ωor 270Ω resistor; (LV)TTL-Normal optical input levels to the receiver result in logic “1” output,pull-up 10kΩresistor。

Recommended Circuit Schematic

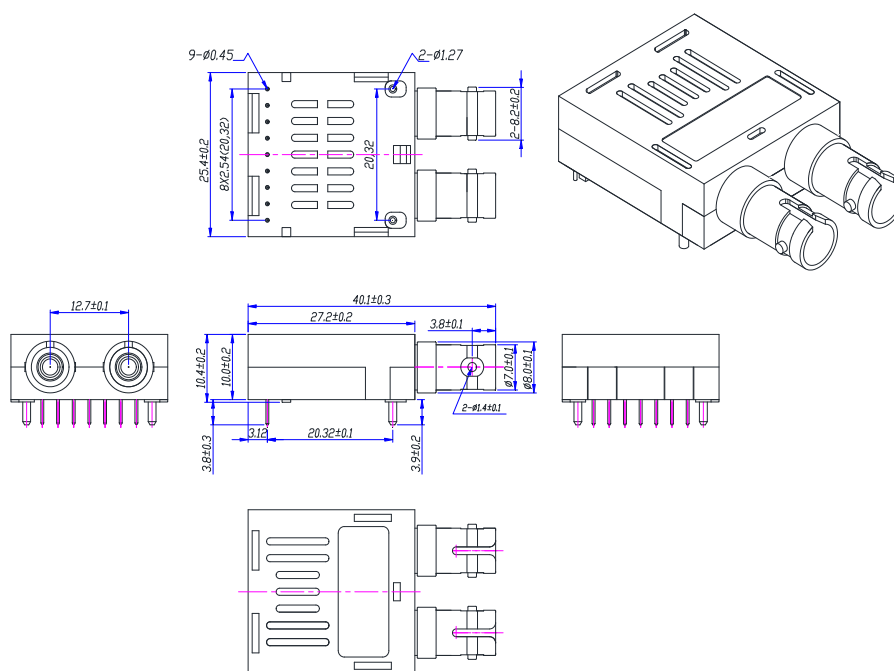


- C1/2/3 = 4.7 μ F
- L1/2 = 1 μ H
- R1/2/3/4 = Depends on SerDes chip used
- R7/8 = Biasing (depends on SerDes chip)
- R9 = open (5 V/3.3 V TTL)
- = 510 Ω (5 V PECL)
- = 270 Ω (3.3 V PECL)

Place R1/2/3/4/7/8 close to SerDes chip.
Place R5/6 close to 1x9 transceiver.

Recommended Circuit Schematic 3.3V SD LVTTTL

Module Outline Drawing (Units in mm)



ST receptacle TYPE1(common)

Order Information

Type	OPTICAL INTERFACE	Fiber Type	Wavelength(nm)	Fiber Length(Km)	Note
L9-TD8501-02CTC	duplex ST	MM	850(VCSEL)	2	(LV)TTL signal detect , 3.3V Supply Voltage, 0 ~ 70℃
L9-TD3101-02CTC	duplex ST	MM	1310(FP)	2	
L9-TD3101-20CTC	duplex ST	SM	1310(FP)	20	
L9-TD3101-40CTC	duplex ST	SM	1310(FP)	40	
L9-TD5501-20CTC	duplex ST	SM	1550(FP)	20	
L9-TD5501-40CTC	duplex ST	SM	1550(DFB)	40	
L9-TD5501-80CTC	duplex ST	SM	1550(DFB)	80	
L9-TD5501-A0CTC	duplex ST	SM	1550(DFB)	100	
L9-TD5501-A2CTC	duplex ST	SM	1550(DFB)	120	